

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2015-16
III B.Tech II Semester ECE Section-C

Room No : 405 North Block (NB)

w.e.f : 21-12-2015

Week 11 - 12 2018				Week 12 - 13 2018			
Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	IPR	MPMC(G1-405) VLSI(G2-403) DSP(G3-411)	VLSI	LUNCH	<-----MPMC/DSP LAB----->		
Tuesday	MEFA	MPMC	VLSI		IPR(G1-405) MEFA(G2-403) DC(G3-411)	DSP	DC
Wednesday	<-----DSP/MPMC LAB----->				IPR	VLSI(G1-405) DSP(G2-403) MPMC(G3-411)	DSP
Thursday	DC	DC(G1-405) IPR(G2-403) MEFA(G3-411)	MEFA		MPMC	VLSI	IPR
Friday	MPMC	DSP	DSP(G1-405) MPMC(G2-403) VLSI(G3-411)		DC	VLSI	MEFA
Saturday	DSP	DC	IPR		MEFA	MEFA(G1-405) DC(G2-403) IPR(G3-411)	MPMC

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics & Financial Analysis	M. Sangeetha	MBA
2	IPR	Intellectual Property Rights	C. Srinivas	ECE
3	DC	Digital Communications	G.Srilakshmi	ECE
4	VLSI	VLSI Design	R.Radhika	ECE
5	MPMC	Microprocessors & Microcontrollers	Asma Begum	ECE
6	DSP	Digital Signal Processing	B. Eleena	ECE
7	MPMC LAB	Microprocessors & Microcontrollers Lab	Asma Begum, K.Amtul Salam	ECE
8	DSP LAB	Digital Signal Processing Lab	B. Eleena,K.Madhu Krishna/ A. Jyothirmai	ECE

Tutorial Groups :

G1:4C1-4E4

G2: 4E5-4G8

G3: 4G9-L436